

**Remarks/Arguments**

**35 U.S.C. §103**

Claims 1-4, 11-15 (apparatus), and 5-10 (method), stand rejected under 35 U.S.C. §103(a) as being unpatentable over Hirvilampi (U.S. Patent No. 6,351,189).

It is respectfully asserted that Hirvilampi fails to disclose both:

“memory means for storing digital values based upon the crest factors associated with a plurality of types of modulation,”

and:

“amplifying means for amplifying the transmission signal controlled by the analog signal converted from said retrieved digital value based on the crest factor corresponding to the type of modulation associated with a transmission signal, decreasing bias current when decreasing the efficiency per bit of the digital modulation and vice versa,”

as described in currently amended claim 1.

Hirvilampi teaches “a method and apparatus for auto-biasing an amplifier. The auto-bias system of the present invention has an auto-bias feedback loop that adjusts the bias condition of an amplifier to a wanted state between transmission periods. The system monitors a physical quantity indicative of the operating state of the amplifier and controls the amplifier bias so as to control the amplifier operating point sufficiently to compensate for variations in amplifier electrical characteristics, amplifier load, amplifier temperature, and input signals. The system further adjusts the amplifier operating point based on the modulation scheme used to modulate information included in an input signal provided to amplifier, thereby allowing the amplifier to operate in any one of multiple signal modulation systems.” (Hirvilampi Abstract)

Hivilampi makes no mention of storing digital values based upon crest factors or converting such digital values to analog signals for amplifier control. Thus, Hirvilampi fails to disclose both “memory means for storing digital values based upon the crest factors associated with a plurality of types of modulation,” and “amplifying means for amplifying the transmission signal controlled by the analog signal converted from said retrieved digital value based on the crest factor corresponding to the type of modulation associated with a transmission signal, decreasing bias current when decreasing the efficiency per bit of the digital modulation and vice versa,” as described in currently amended claim 1.

In view of the above remarks, it is respectfully submitted there is no 35 USC 112 enabling disclosure provided by Hirvilampi, which makes the present invention as claimed in claim 1 unpatentable. It is further submitted that independent claims 5 and 11 are allowable for at least the same reasons that claim 1 is allowable. Since dependent claims 2-4, 6-10, and 12-15 are dependent from allowable independent claims 1, 5, and 11 respectively, it is respectfully submitted that they too are allowable for at least the same reasons that their respective independent claims are allowable. Thus, it is further respectfully submitted that this rejection has been satisfied and should be withdrawn.

Having fully addressed the Examiner’s rejections it is believed that, in view of the preceding amendments and remarks, this application stands in condition for allowance. Accordingly then, reconsideration and allowance are respectfully solicited. If, however, the Examiner is of the opinion that such action cannot be taken, the Examiner is invited to contact the applicant’s representative at (609) 734-6804, so that a mutually convenient date and time for a telephonic interview may be scheduled.

No fee is believed due. However, if a fee is due, please charge the additional fee to Deposit Account 07-0832.

Respectfully submitted,  
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